

Amendments to the Specification:

Please replace paragraph [0031] with the following amended paragraph:

[0031] More specifically, analog front-end 15 includes coarse gain control functions 16a, 16b, which apply a coarse AGC correction to the signals received by interface 14. Offset compensation (not shown) may also be applied at this point. As will be described in further detail below, coarse gain control functions 16 operate in the analog domain, yet are digitally controlled. The analog ~~output~~ outputs of coarse gain control functions 16a, 16b are then applied to analog-to-digital converters (ADCs) 18a, 18b, respectively, which sample the amplified analog signals and convert them into the digital domain, as known in the art. Preferably, ADCs 18 are at least of ten-bit resolution, for high-fidelity and high-definition video display functionality. The video signals, now in the digital domain in the form of sampled datastreams, are then applied to respective digital front-end AGC functions 20a, 20b, which apply a finer AGC correction to the digital video signals. According to the preferred embodiment of the invention, digital front-end AGC functions 20a, 20b set their gains by way of feedback measurements of a selected signal attribute, as will be described in further detail below. Offset compensation (not shown) may also be performed after digital front-end AGC functions 20. Input format circuit 24 comprehends the blanking intervals and other synchronization information contained within the video signals, for the particular signal standard or format received from digital front-end AGC functions 20a, 20b, and strips the displayable information (e.g., luma and chroma) from the incoming video signals, in the conventional manner.